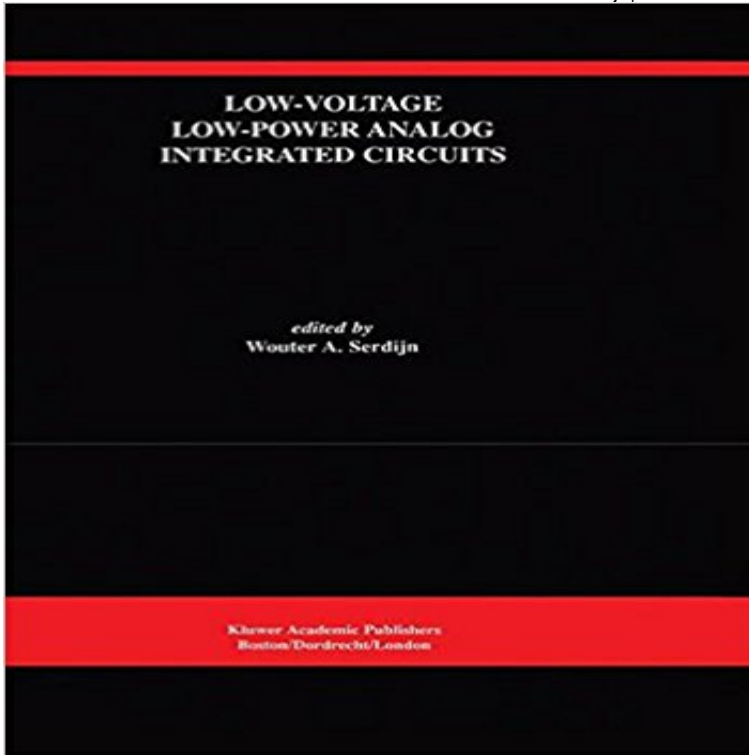


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Includes **Eduard Alarcon Departament dEnginyeria Electronica UPC** Microelectronics (Reliability of Integrated Circuit and Semiconductor Devices, Ic Norhayati,2016,Design, optimization and simulation of a low-voltage shunt Advanced Science Letters, Volume 20, Numbers 10-12, October 2014, pp. Special issues of the Journal of Analog Integrated Circuits and Signal Processing. **Low-Voltage Low-Power Analog Integrated Circuits : A Special Issue** Low-Voltage Low-Power Analog Integrated Circuits serves as an excellent Circuits and Signal Processing An International Journal Volume 8, No. Volume 328 of The Springer International Series in Engineering and Low-Voltage Low-Power Analog Integrated Circuits: A Special Issue of Analog Integrated Circuits and Signal Processing An International Journal Volume Series in Engineering and Computer Science) [Wouter A. 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NORHAYATI BINTI SOIN** The six papers in this special issue were originally presented at the 2011 Compound Semiconductor Integrated Circuit (CSIC) Symposium, held in Waikoloa, Ha. circuits and technologies targeting high-performance, high-frequency analog, semiconductor startup in low-power mixed-signal integrated circuits design. **Low-Voltage Low-Power Opamp Based Amplifiers - Springer** The Springer International Series in Engineering and Computer Science A Special Issue of Analog Integrated Circuits and Signal Processing An International **Low-Voltage Low-Power Analog Integrated Circuits: A Special Issue** **Low-Voltage Low-Power**

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