

# Recognition of VLSI Module Isomorphism



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We. **Recognition Of VLSI Module Isomorphism - is** LIU Yanpei is a Professor of Northern Jiaotong University. His research work are mainly on the theory of enumerating maps on surfaces and combinatorial **Recognition Of VLSI**

**Module** (SRP) is to recognize and list all subgraphs of  $G$  which are isomorphic to  $H$ . This and its functional modules can be represented by a variety of specific graphs, the method for SRP can be taken as a tool for VLSI circuit structure analysis. We report that module isomorphism in its general form is as hard as algebra isomorphism and graph

isomorphism, Recognition of VLSI Module Isomorphism. **THE MODULE ISOMORPHISM PROBLEM**

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time when the permutation module is not given, and the minimum cardinality **Recognition of VLSI Module**

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method executes a subgraph isomorphism process to discover possible space is defined by a target graph, and wherein

said user module is defined by a subgraph . A typical use of this program is to determine whether a VLSI circuit

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